U.S. DEPT. OF COMMERCE PATENT AND TRADEMARK OFFICE

Form PTO-1449 **LIST OF PRIOR ART CITED BY APPLICANT** *(Use several sheets if necessary)*	ATTORNEY DOCKET NO. AUS920010672US1	SERIAL NO. 09/965,000
	APPLICANT **Lee et al.**	
	FILING DATE **September 27, 2001**	GROUP ART UNIT **2133**

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
[init]	AA 4564903	Jan. 14, 1986	Guyette et al.	364/300	Oct. 5, 1983
[init]	AB 4843541	Jun. 27, 1989	Bean et al.	364/200	Jul. 29, 1987
[init]	AC 5345590	Sep. 6, 1994	Ault et al.	395/650	Sep. 1, 1993

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

OTHER PRIOR ART *(including author, title, date, pertinent page, etc.)*

[init]	AD	IBM Technical Disclosure Bulletin, "Hypervisor High Performance Synchronous Dispatch for Coupled Systems", Vol. 39, No. 12, December 1996
[init]	AE	IBM Technical Disclosure Bulletin, "VM MPG Operating as a DRF Hypervisor as a First Level Guest Under PR/SM", Vol. 38, No. 04, April 1995, p. 325
[init]	AF	IBM Technical Disclosure Bulletin, "Sharing Read-Only Memory among Multiple Logical Partitions", Vol. 36, No. 03, March 1993, pp. 303-304
[init]	AG	IBM Technical Disclosure Bulletin, "Highly Parallel Coupling Facility Emulator/Router with Shadowed Link Buffers", Vol. 39, No. 12, December 1996
[init]	AH	IBM Technical Disclosure Bulletin, "Coordinating Multiple Server Partitions to Enter Power-Save State", Vol. 39, No. 06, June 1996, pp. 235-239.

DATE CONSIDERED 8/2/04	EXAMINER *Joshua Lohn*

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.